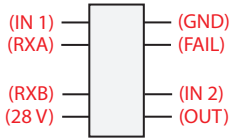




ARINC 429 CONVERTER

LOGIC & INTERFACE

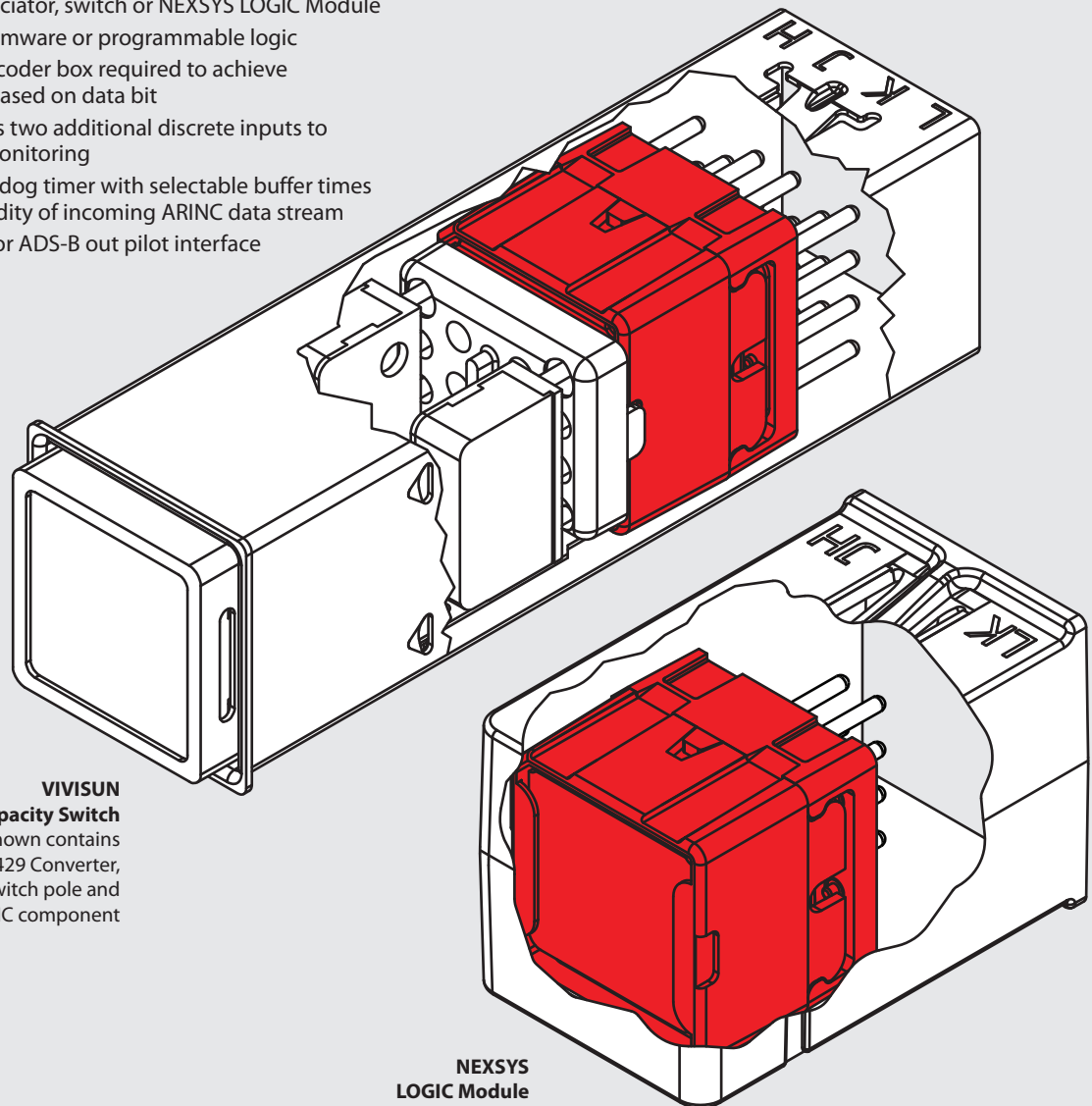


SR429/1M

The ARINC 429 Converter component is an 8-pin logic and interface device developed as a part of our NEXSYS® LOGIC Component Technology. This single-bit, single-label ARINC 429 Converter can replace the need for a dedicated ARINC converter box. The component can be integrated into a VIVISUN® annunciator or switch, and can also be specified in a stand-alone NEXSYS LOGIC Module for in-line use behind the panel. The ARINC 429 Converter can be combined with electromechanical switches and other LOGIC components to create a custom configuration that uniquely addresses the designer's specific functional requirements. The ARINC 429 Converter is designed and tested in accordance with MIL-PRF-22885 and DO-160.

SR429/1M

- Industry first innovation - ARINC 429 converter inside VIVISUN annunciator, switch or NEXSYS LOGIC Module
- No software, firmware or programmable logic
- No external decoder box required to achieve annunciation based on data bit
- Accommodates two additional discrete inputs to provide "fail" monitoring
- Includes watchdog timer with selectable buffer times to monitor validity of incoming ARINC data stream
- Ideally suited for ADS-B out pilot interface requirements



**VIVISUN
High Capacity Switch**
as shown contains
ARINC 429 Converter,
single switch pole and
4-pin LOGIC component

**NEXSYS
LOGIC Module**
as shown contains
ARINC 429 Converter

ARINC 429 Converter (SR429/1M)

The latest innovation from Applied Avionics is an ARINC 429 Converter (SR429/1M) component that is housed inside a VIVISUN® annunciator, switch or NEXSYS® LOGIC Module. While this can offer a wide range of application solutions, the FAA mandate requiring pilot ADS-B Out system status notifications make this product an economical solution to meet this important industry requirement.

Benefits: The SR429/1M provides the ability to convert a single data bit from an ARINC 429 data bus to a discrete signal within an annunciator, switch or LOGIC module.

- Can be specified for any single-bit on any single label
- Other options include ARINC transmission speeds, parity checking, and discrete output type (Active Low or Active Open)
- No DO-178 or DO-254 issues since the device does not use software or firmware
- Internal watchdog timer to monitor validity of incoming ARINC data stream
- Accommodates two additional external signal inputs with discrete output on “fail”
- Reduces circuit complexity, size, weight, and installation labor

Application: The SR429/1M is a single bit ARINC 429 converter, with additional inputs for up to two independent discrete analog signals.

The typical application for the SR429/1M is for failure indications, although the device can be used for a wide range of ARINC data to analog conversion situations. What makes the SR429/1M device unique is its ability to reside inside of an annunciator, switch or LOGIC Module. See “Application Examples” (pages 4 - 5) for schematics of the following examples.

In Application 1, the SR429/1M is performing ADS-B Out failure annunciation. Integrated SSRs are utilized to gate the signal between the two transponders, the two SR429/1Ms and the LED annunciators. In this situation, a discrete analog input (i.e. transponder health) is being monitored as well as the ARINC 429 data information. This affords dual redundancies by monitoring each of the transponders, with the “transponder select” switch determining which transponder status is being shown on the annunciators. The watchdog timer is being employed to monitor the validity of the ARINC data. This application takes advantage of a second (co-pilot) annunciator.

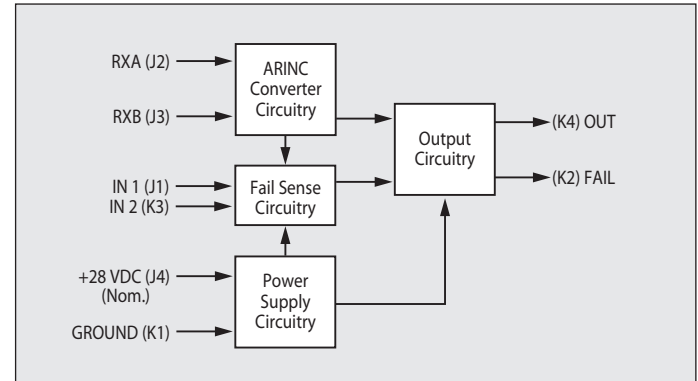
Application 2 is a simpler demonstration of the SR429/1M where the unit is used to provide a discrete annunciation of two states provided by the system controller. The internal watchdog timer is used to ensure proper performance of the SR429/1M converter and the validity of the data being received. Neither of the other optional inputs are being utilized in this example.

Application 3 demonstrates the SR429/1M device used as an in-line ARINC converter inside a NEXSYS LOGIC Module for situations where no annunciation is required. The single bit can be read off of the ARINC data bus and converted to either an Active Low (ground) or Active Open (high impedance) when the selected data bit is 1.

LOGIC Component Technology Overview: The SR429/1M is a powerful addition to our innovative NEXSYS brand of LOGIC Component Technology designed to enhance avionics design flexibility. LOGIC components include a wide variety of electronic switching, electronic sensing/detecting and logic/interface devices that allow designers to create mix-and-match solutions to address their system integration challenges. Circuit complexity, size, weight and installation labor are all reduced by providing the ability to internally integrate multiple combinations of LOGIC components inside a single switch, annunciator, or module housing.

The SR429/1M can be combined with up to two additional 4-pin NEXSYS LOGIC components or electromechanical switches in a High Capacity switch or annunciator body with an illuminated lens cap. When used in the same body as an SR429/1M, all other LOGIC components and electromechanical switches are limited to a maximum of 1.0 amp current on any pin. When used in a LOGIC Module, the SR429/1M occupies the entire module with no additional capacity.

Figure 1: Block Diagram



ARINC Converter Circuitry: Inputs J2 and J3 supply the ARINC converter circuitry with the 2 wire ARINC signal input. The following options must be defined at the time of order:

- Any single ARINC Label (000 – 377)
- Any single data bit from selected label (11 – 31)
- ARINC transmission speed (High or Low)
- ARINC bit output type (Pin K4)
 - Can be specified to be Active Low or Active Open when selected data bit = 1. See *Table 2*
- Handling of SDI Bits (Bits 9 & 10)
- ARINC parity checking (On or Off)

Fail Sense Circuitry: The internal fail sense circuitry simultaneously monitors the operation of the internal ARINC converter and two optional external inputs and drives a single discrete output on Pin K2 (FAIL). Pin K2 (FAIL) will be normal ground and become Open (high impedance) upon a) a “fail” signal from the internal health monitor or b) an indication from either of the optional external inputs.

The following events would cause the internal health monitor to provide a “fail” signal and cause Pin K2 to go Open: a) failure of the ARINC converter, b) loss of power to the SR429/1M unit, or c) loss of valid ARINC data input. The health monitor includes a watchdog timer requiring a valid ARINC label to be received within a specified time buffer. Desired buffer time specification must be selected at time of order:

- Buffer Timing (0.5s, 1.0s, 1.75s, 2.5s, 5.0s, 10.0s, 15.0s)

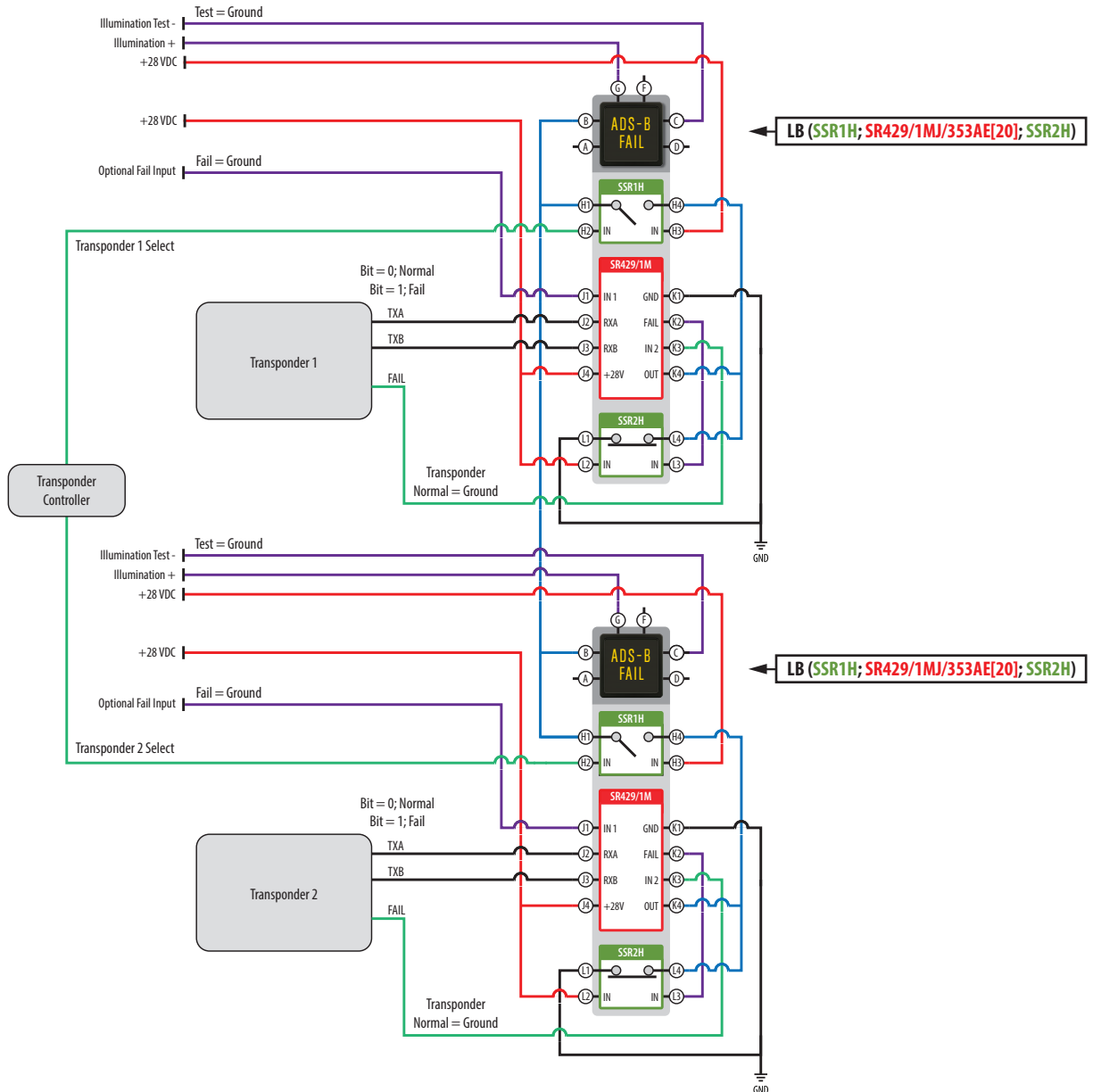
Either of the optional orthogonal external inputs can also provide a “fail” indication and cause Pin K2 to go Open. Pin J1 (IN 1) is pre-defined as Normal = Power or Open, Fail = Ground, therefore a Ground on Pin J1 (IN 1) would create an Open on Pin K2 (FAIL). Pin J1 can remain open if unused. Pin K3 (IN 2) is pre-defined as Normal = Ground, Fail = Open or Power, therefore an Open or Power on Pin K3 (IN 2) would create an Open on Pin K2 (FAIL). Pin K3 must be grounded if unused.

Output Circuitry: Pins K2 and K4 are open drain outputs. Pin K4 (OUT) is the decoded ARINC 429 output bit related directly to the ARINC converter circuitry. Pin K2 (FAIL) is the result of the fail sense circuitry, and is related to status of the internal health monitor and the status of Pin J1 (IN 1) and Pin K3 (IN 2).

Application Examples

Application 1

Schematics shown for LED illuminated caps.
SR429/1M not compatible with incandescent caps.



In this application, two identically configured annunciators each contain an SR429/1M ARINC converter (Active Low Configuration), a normally open SSR (SSR1H) and a normally closed SSR (SSR2H). Each transponder is being monitored independently by sending ARINC 429 data stream (Bit = 0; Normal, Bit = 1; Fail) and a discrete analog output for "transponder failure" (Normal = Ground, Fail = Open) to its respective SR429/1M.

The Transponder Controller determines which transponder is "active" by grounding Pin H2 of the top SSR1H to make Transponder 1 active or by grounding Pin H2 of the bottom SSR1H to make Transponder 2 active. If a failure exists on the "active" transponder, the status is displayed on both annunciators. The status of the "inactive" transponder is not reported on the annunciator legends.

Ground can be applied to Pin H4 of the "active" transponder, illuminating both legends, in three different ways:

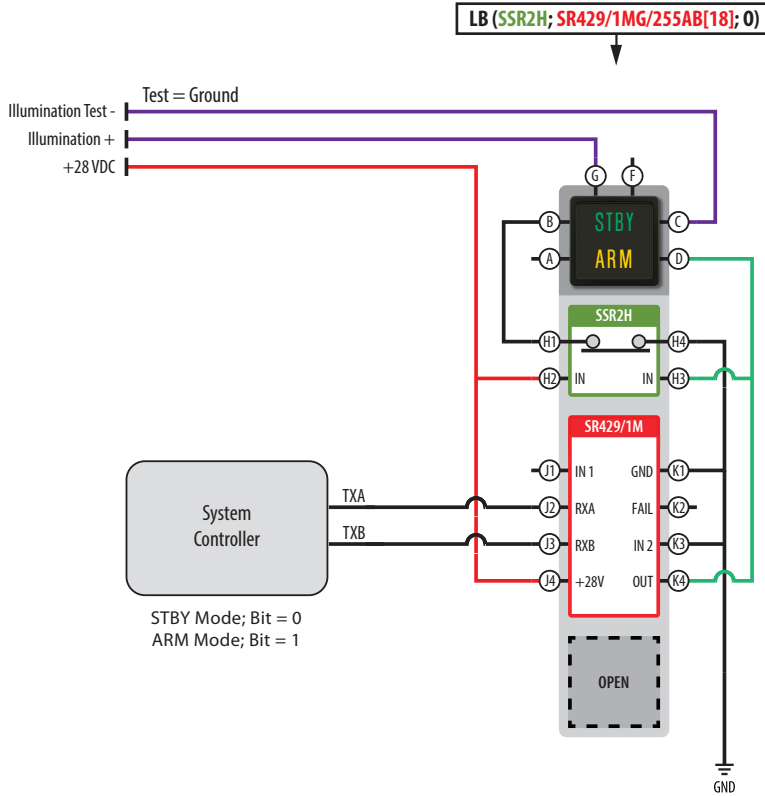
1. GPS failure is transmitted to SR429/1M as Bit =1, creating a Ground output on OUT (Pin K4) due to this SR429/1M being configured as Active Low (Ground). Ground on Pin K4 is tied directly to Pin H4.
2. If transponder fails, IN 2 (Pin K3) is Open, creating an Open on output FAIL (Pin K2), and the SSR2H acts as a drop out relay and will close, passing a ground signal to Pin H4. If transponder is good, IN 2 (Pin K3) is Ground (normal), therefore K2 is ground (normal) and the normally closed SSR2H is held open and no signal is passed on.
3. If a valid ARINC message is not received by the SR429/1M within the specified buffer timing, or the SR429/1M unit loses power, FAIL (Pin K2) will become Open, the SSR2H acts as a drop out relay and will close, passing the ground signal to Pin H4.

Disclaimer: The configurations and diagrams shown above are provided by Applied Avionics, Inc. as a general example only. The recipient is solely responsible for actual design, electrical wiring, validation, testing, applicability and functionality of the product in regards to the customer's specific application.

Application Examples

Application 2

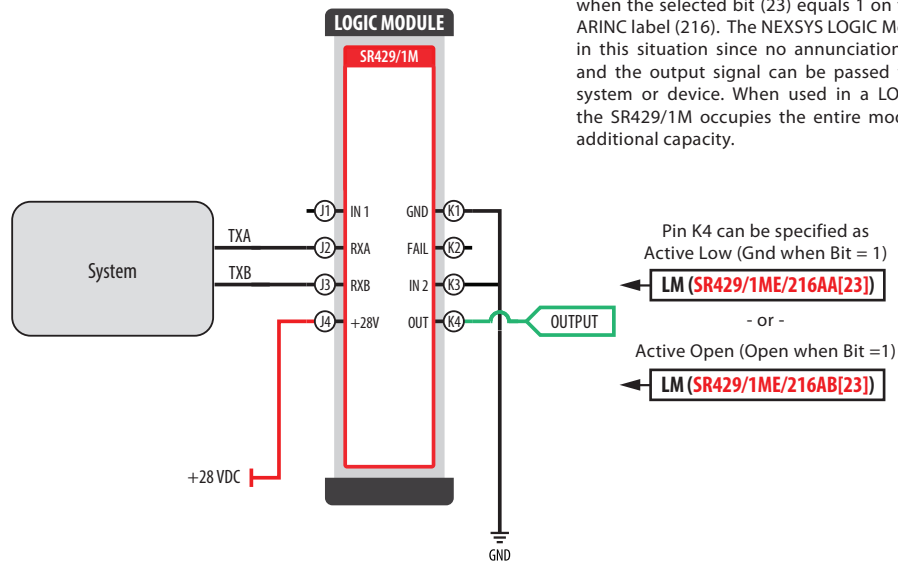
Schematics shown for LED illuminated caps.
SR429/1M not compatible with incandescent caps.



In this application, the ARINC data bit from the system controller equals 0 in the STBY mode and the data bit equals 1 in the ARM mode. The SR429/1M specified in this application is an "Active Low" configuration (see Table 2) where the output on PIN K4 (OUT) is Open when the data bit equals 0 (equivalent to the STBY mode) and is Ground when the data bit equals 1 (equivalent to the ARM mode).

When system controller is in the STBY mode (ARINC bit = 0) the output of PIN K4 (OUT) of the specified SR429/1M component (specified as Active Low Configuration) is open, which illuminates the upper "STBY" legend through the normally closed SSR2H relay contacts. When system controller is in the ARM mode (ARINC bit = 1) the output of PIN K4 (OUT) becomes Ground, which will illuminate the "ARM" legend from PIN K4 (OUT) and extinguish the "STBY" legend by opening the normally closed SSR2H relay. Opposite polarity options are available.

Application 3



In this application, the SR429/1M component is being used to simply generate a ground or open signal when the selected bit (23) equals 1 on the specified ARINC label (216). The NEXSYS LOGIC Module is used in this situation since no annunciation is required, and the output signal can be passed to any other system or device. When used in a LOGIC Module, the SR429/1M occupies the entire module with no additional capacity.

Disclaimer: The configurations and diagrams shown above are provided by Applied Avionics, Inc. as a general example only. The recipient is solely responsible for actual design, electrical wiring, validation, testing, applicability and functionality of the product in regards to the customer's specific application.

Table 5: SR429/1M Qualification Level Summary

Test Description	Specification	Section	Category	Reference Levels*
Altitude	RTCA/DO-160 MIL-STD-202 MIL-STD-810	4 105C 500	F2/4 B Method II	-15,000 feet, +55,000 feet
Temperature	RTCA/DO-160 MIL-STD-810	4 501/502	F2 Method III	-55°C and +85°C (Illuminated Indicator rated at +71°C)
Temperature Variation	RTCA/DO-160 MIL-STD-202 MIL-STD-810	5 107 503	S2 A 1	5 cycles -55°C /+85°C
High Temperature Survival (Non-operating)	MIL-STD-202	108A	A	+85°C, 96 hours (Switch Module) +125°C, 96 hours (Electronic Unit only)
Humidity	RTCA/DO-160 MIL-STD-810	6 507	B Method II	240 hours, +65°C, > 90% RH
Operational Shock and Crash Safety	RTCA/DO-160 MIL-STD-202 MIL-STD-810	7 213 516	B B N/A	20 G Sawtooth, 20 G Acceleration 75 G Half-Sine
Acceleration	RTCA/DO-160 MIL-STD-202 MIL-STD-810	7 212 513	B A Method III	20 G, 3 axis
Vibration	RTCA/DO-160 MIL-STD-202	8 204	R,U B	10-2000 Hz, 10 G 10-2000 Hz, 15 G
Explosive Atmosphere	RTCA/DO-160 MIL-STD-202	9 109	E	
Waterproofness	RTCA/DO-160 MIL-PRF-22885	10 4.7.20	R	Applies to Sealed Switches only 15 gal/min
Waterproofness	RTCA/DO-160	10	Y/W	Applies to LOGIC module only
Sand and Dust	RTCA/DO-160 MIL-STD-202	12 110	D N/A	Applies to both Sealed Switches and LOGIC Module
Fungus Resistance	RTCA/DO-160 MIL-PRF-22885	13 3.5.2	F N/A	Compliance by material selection
Magnetic Effect	RTCA/DO-160	15	Z	1° deflection, < 0.3 m
Power Input	RTCA/DO-160 MIL-STD-704	16 N/A	A/B N/A	Power Interrupt Level A - 50 ms
Spike/Transient	RTCA/DO-160 MIL-STD-461 MIL-STD-461	17 CS106 CS115	A Spike 1 N/A	Power, 600 V, 10 us, 50 ohm 400 V, 10 us, 5 ohm 30 ns, 5 amp
Audio Frequency Conducted Susceptibility	RTCA/DO-160 MIL-STD-461	18 CS101	Z Curve 2	Power Input, 4 V P-P, 0.01-150 KHz 30 Hz to 150 KHz
Induced Signal Susceptibility	RTCA/DO-160	19	CW	10,000 V/m, 120 A/m, 350-800 Hz
RF Conducted Susceptibility **	RTCA/DO-160 MIL-STD-461	20 CS114	Y Curve 5	300 mA, 10 KHz-400 MHz
RF Radiated Susceptibility **	RTCA/DO-160 MIL-STD-461	20 RS103	Y 200 V/m	200 V/m, 2 MHz-18 GHz
Conducted RF Emissions	RTCA/DO-160 MIL-STD-461	21 CE102	P	150 KHz to 152 MHz 10 KHz to 10 MHz
Radiated RF Emissions	RTCA/DO-160 MIL-STD-461	21 RE102	P	100 MHz-6 GHz 10 KHz-6 GHz
Damped Sinusoidal Transient **	RTCA/DO-160 MIL-STD-461	22 CS116	B3K3L3 N/A	Waveform 3, 600 V, 1 MHz, 10 MHz
Lightning Induced Transient **	RTCA/DO-160	22	B3K3L3	Waveform 5 A, 300 V, 120 us
Dielectric Withstanding	MIL-STD-202	301	N/A	1000 VAC
Electrostatic Discharge	RTCA/DO-160	25	N/A	15,000 V, 150 pf, 330 ohms

* Actual qualification levels vary by component. See specific component data sheet for actual qualifications.

** Stated EMC performance based on tests performed on an individually monitored component using unshielded cables as defined by the applicable EMC test document. The EMC performance of an installed system using LOGIC components can be dependent on the actual installation environment and interconnection method.

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